ABSTRACT OF THE DISCLOSURE

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A microcomputer stores a copy of an IEEE register having a value updated by an XAUI retimer in an IEEE/XENPAK virtual register in accordance with predetermined timing. In response to a request by a MAC layer, the microcomputer outputs the copy of the IEEE register stored in the IEEE/XENPAK virtual register to the MAC layer via a MDIO interface. Therefore, microcomputer can centrally control the contents of the registers, and can rapidly send the value of the register in response to the request by the MAC layer.